

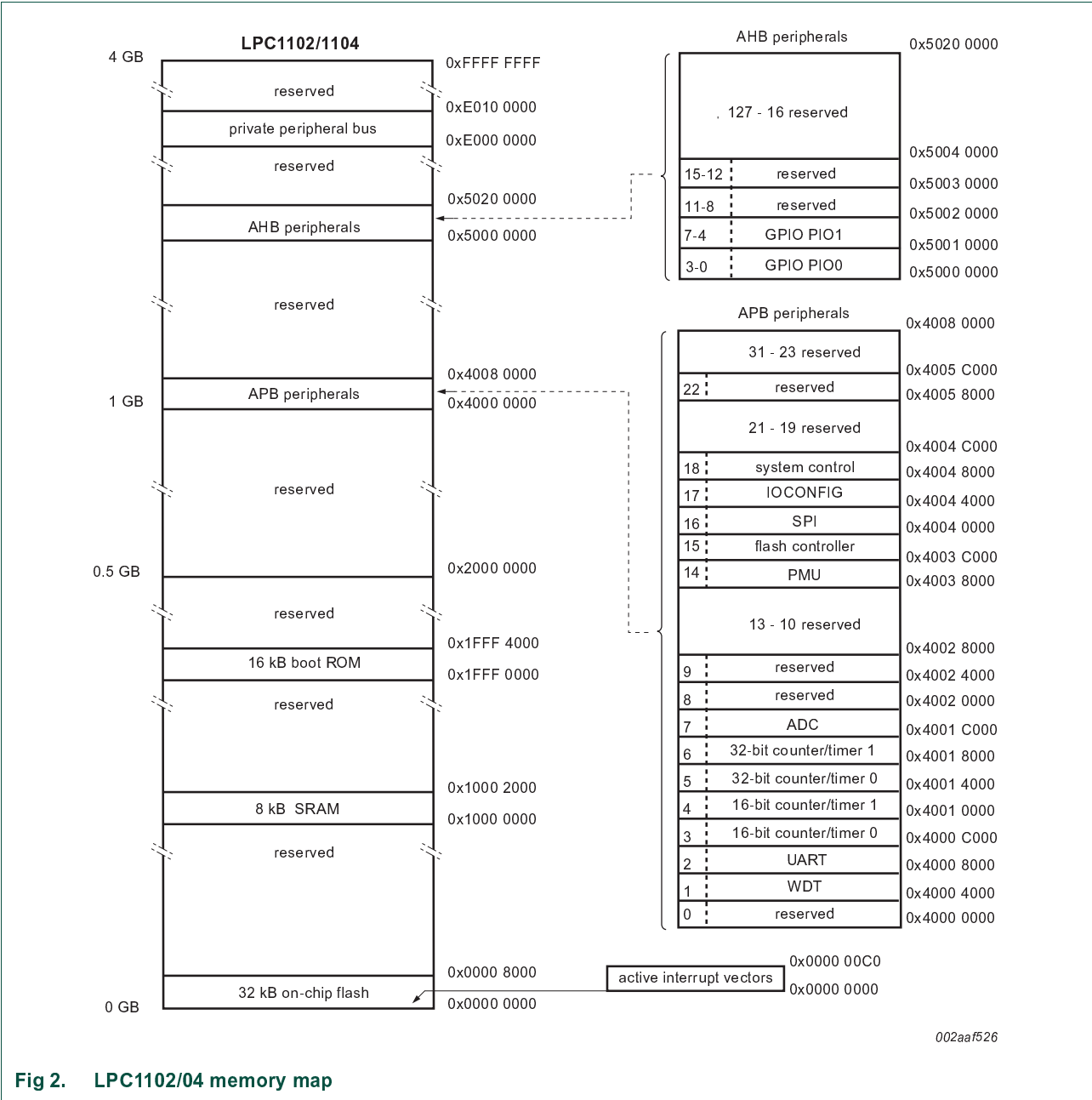


Fig 2. LPC1102/04 memory map