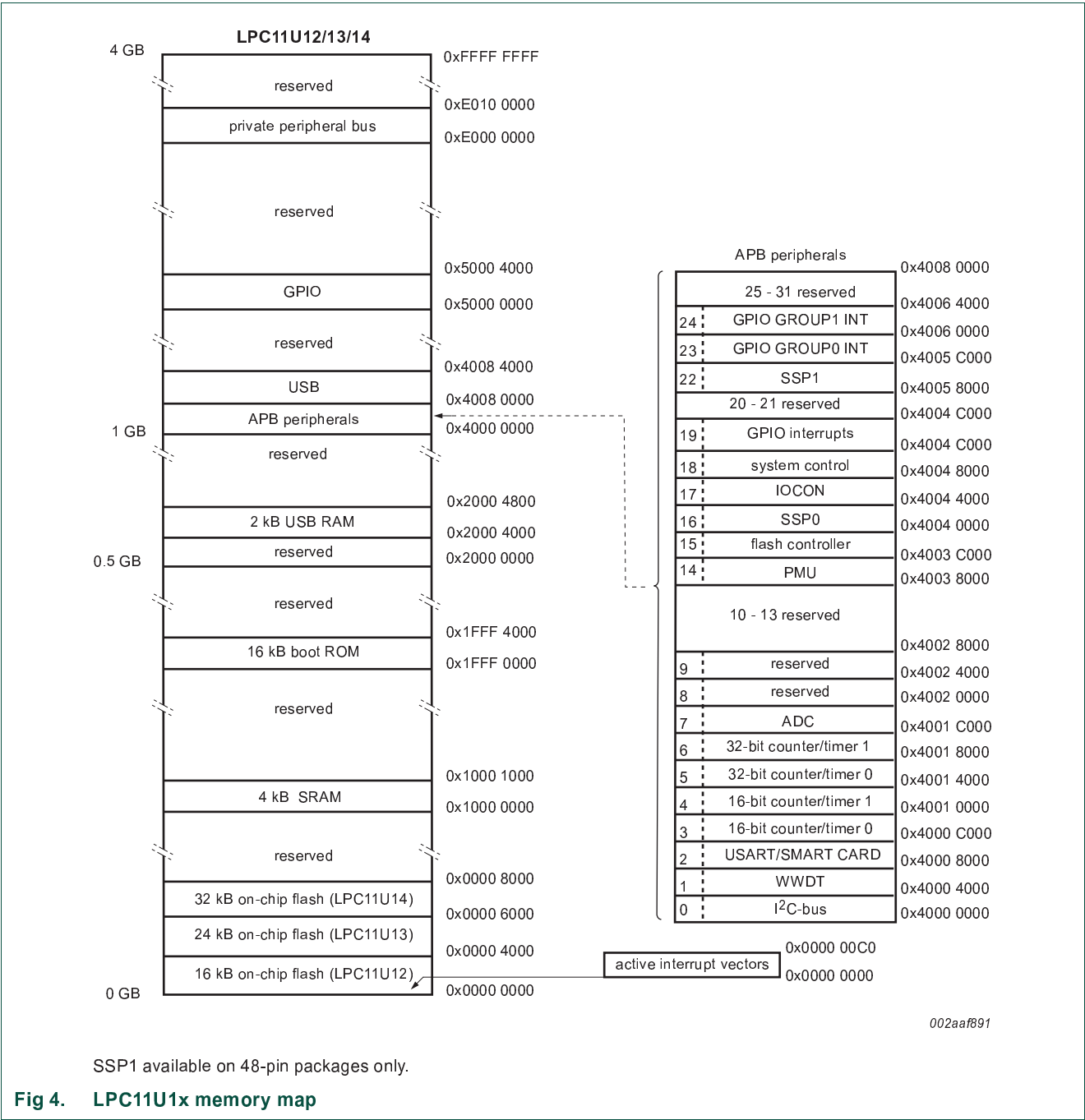






**Fig 5. LPC11U2x memory map**

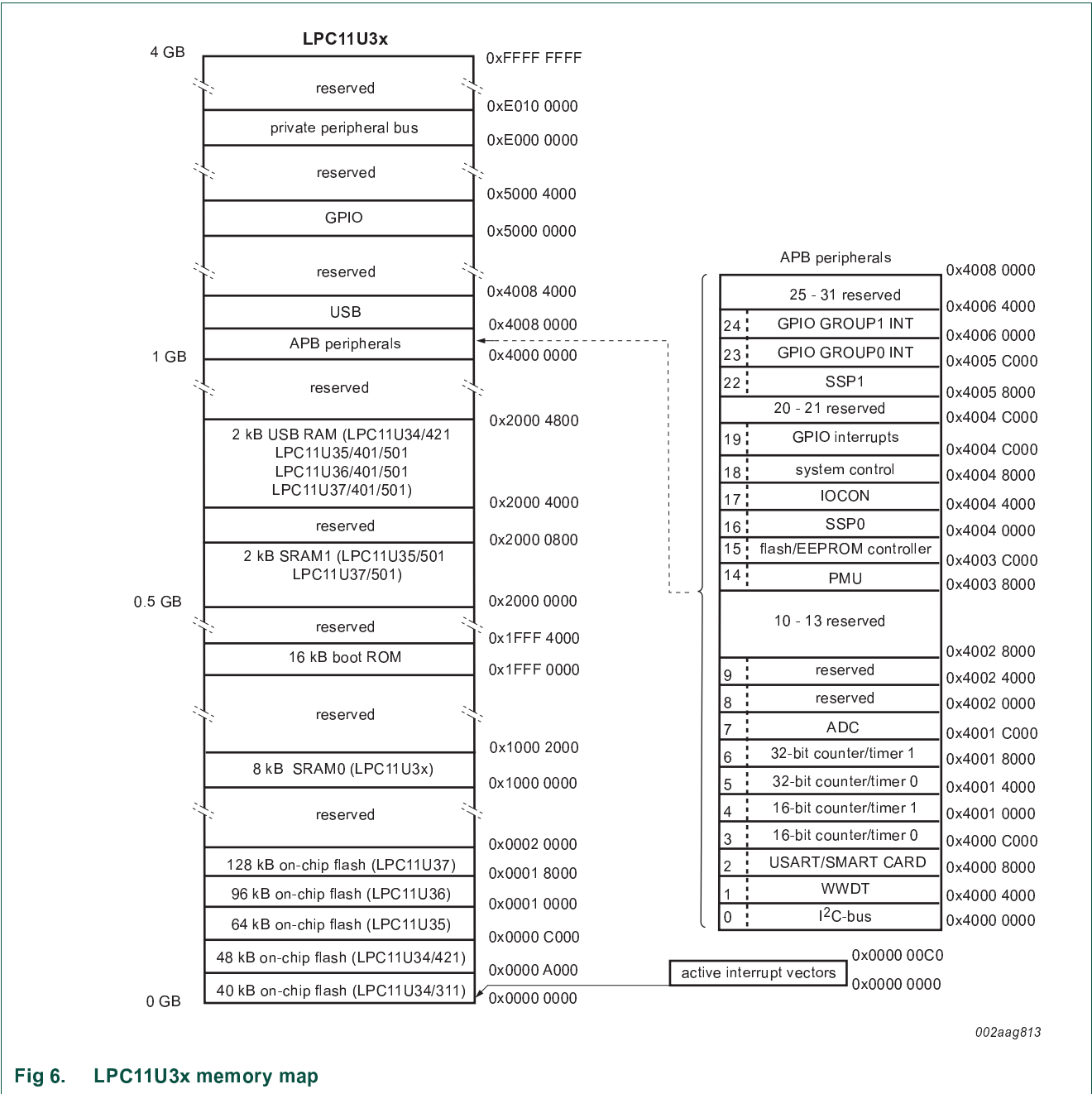


Fig 6. LPC11U3x memory map