

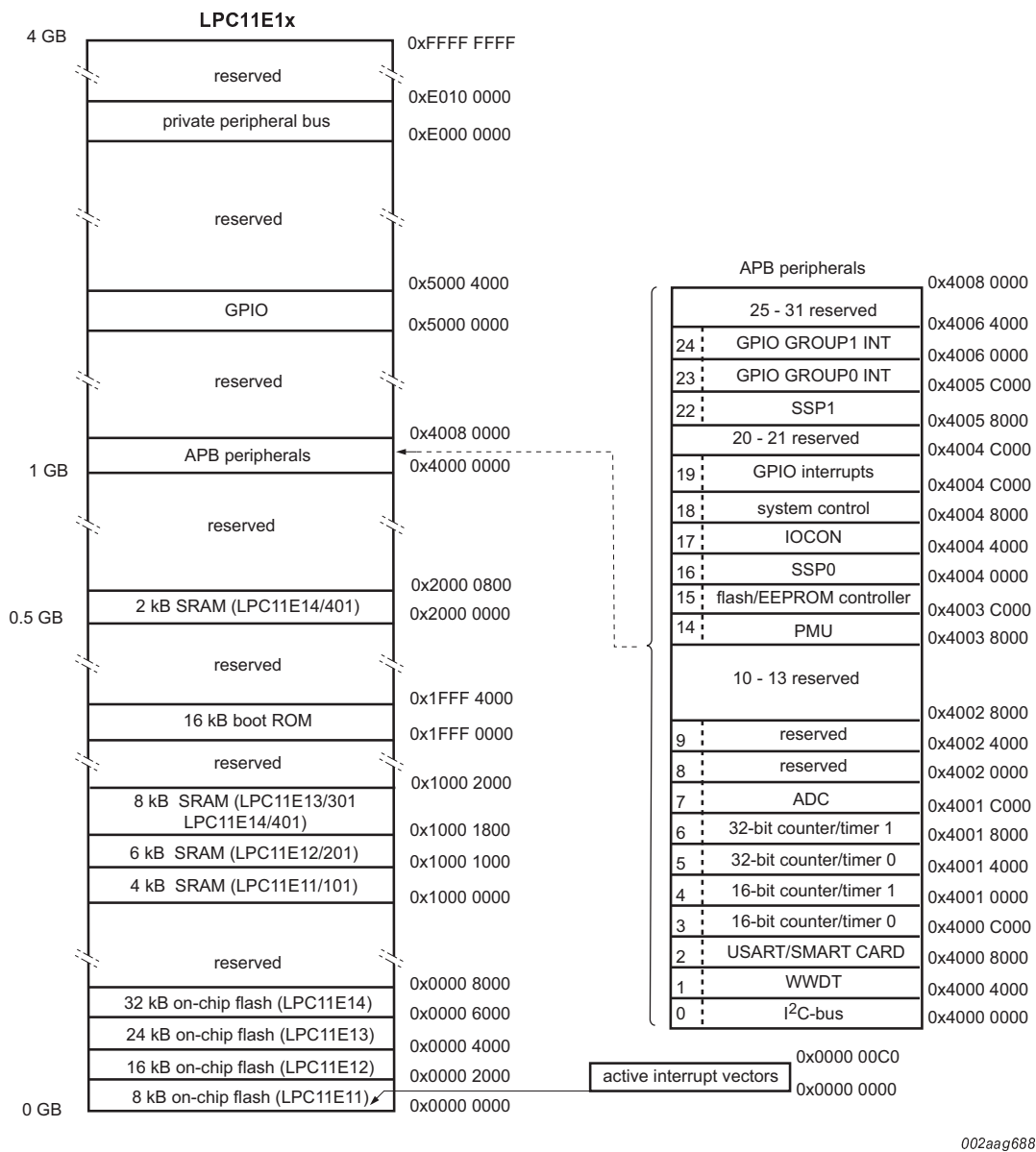


Fig 3. LPC11E1x memory map

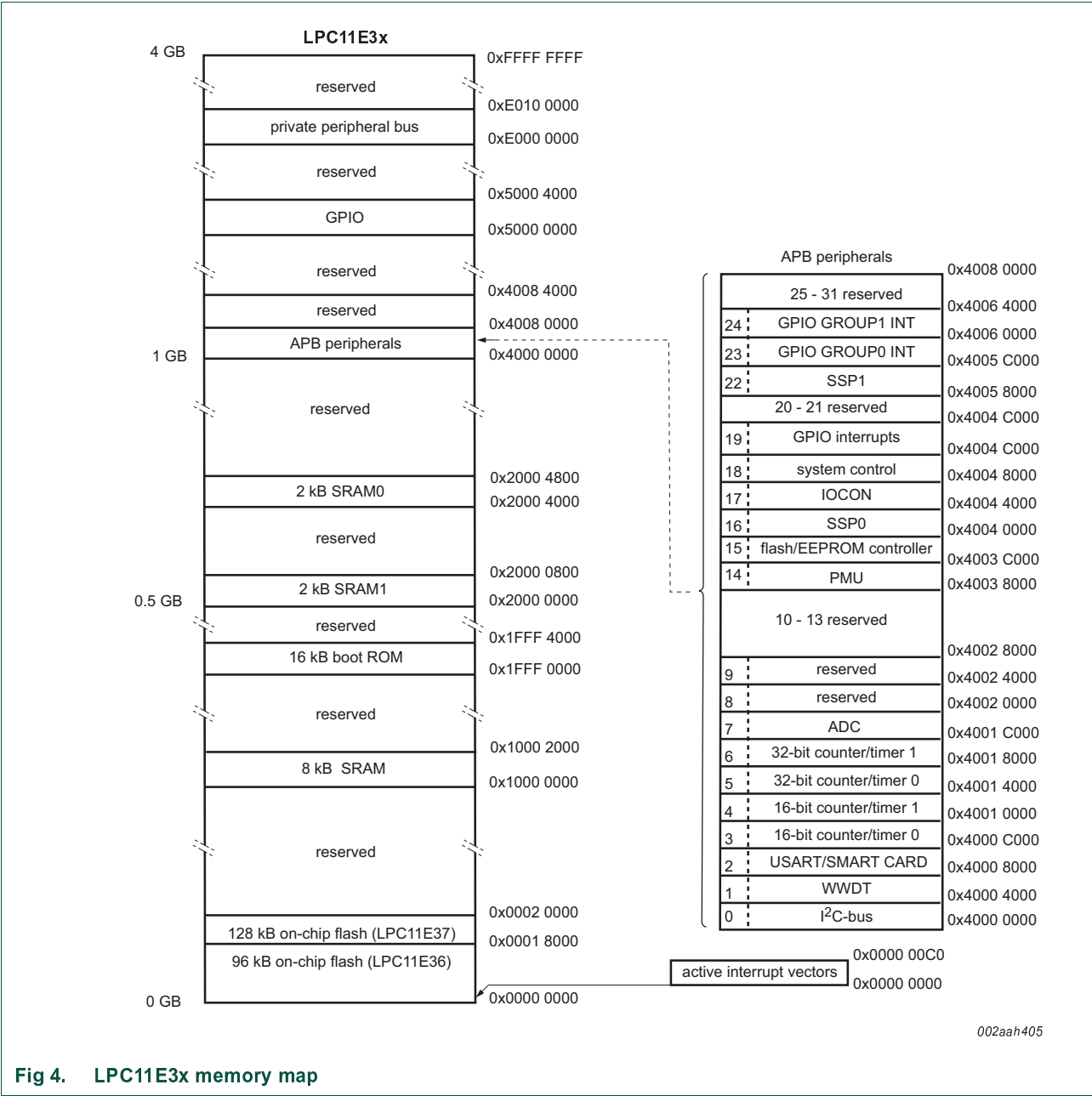


Fig 4. LPC11E3x memory map