

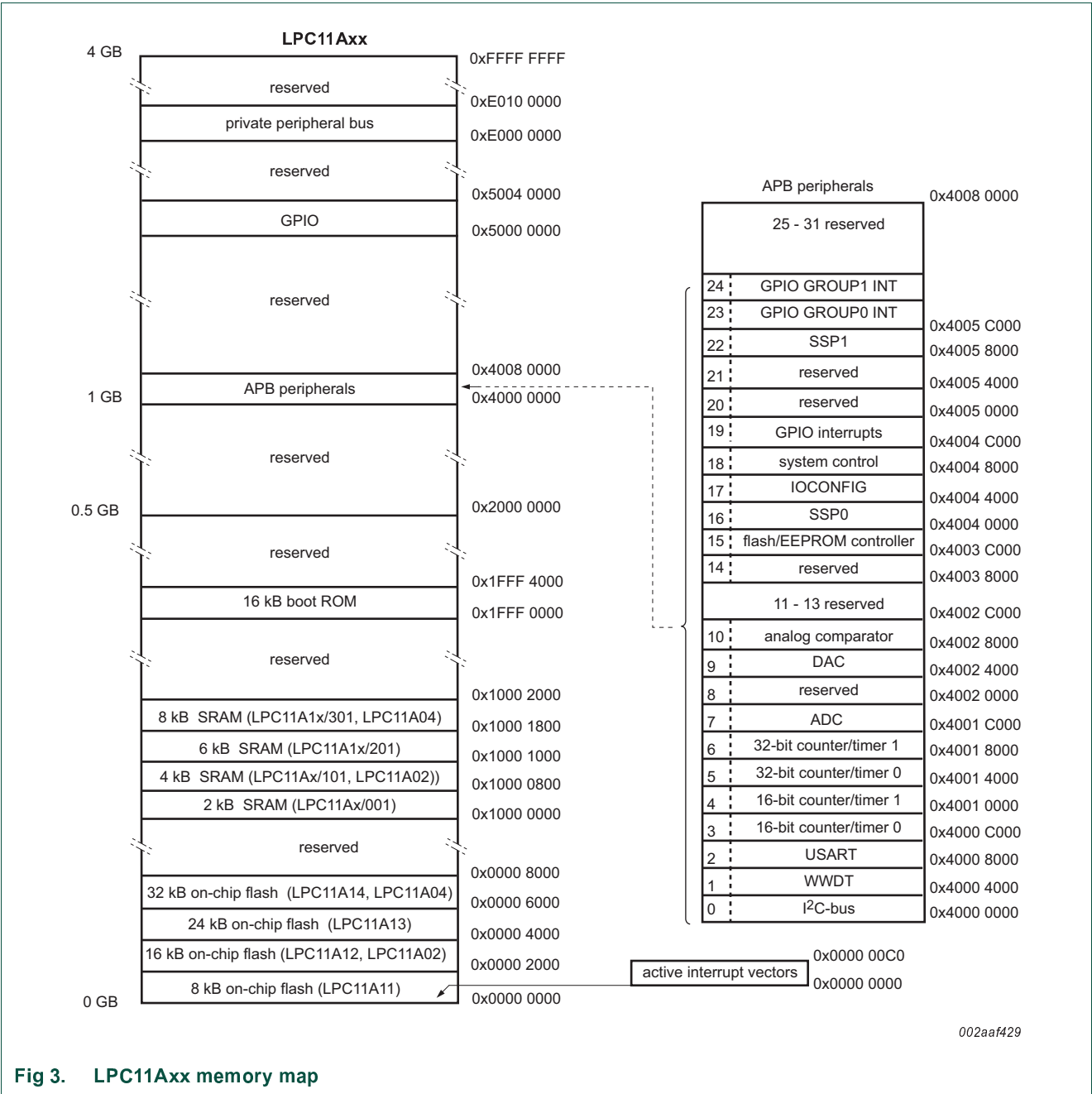


Fig 3. LPC11Axx memory map