

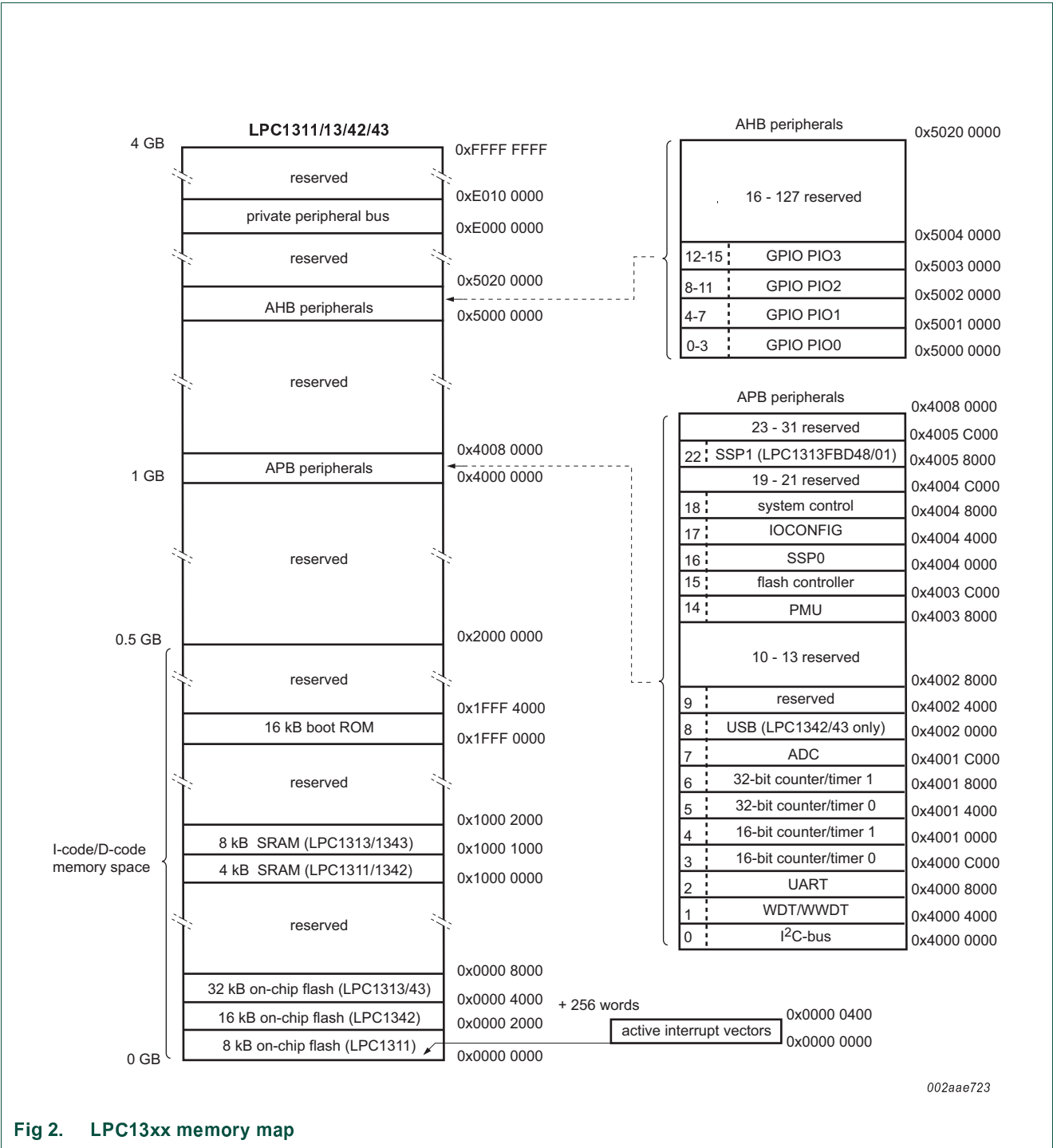


Fig 2. LPC13xx memory map

2.3 Memory remapping

For details, see [Table 8](#).