

2.4 Memory map (flashless parts LPC1850/30/20/10)

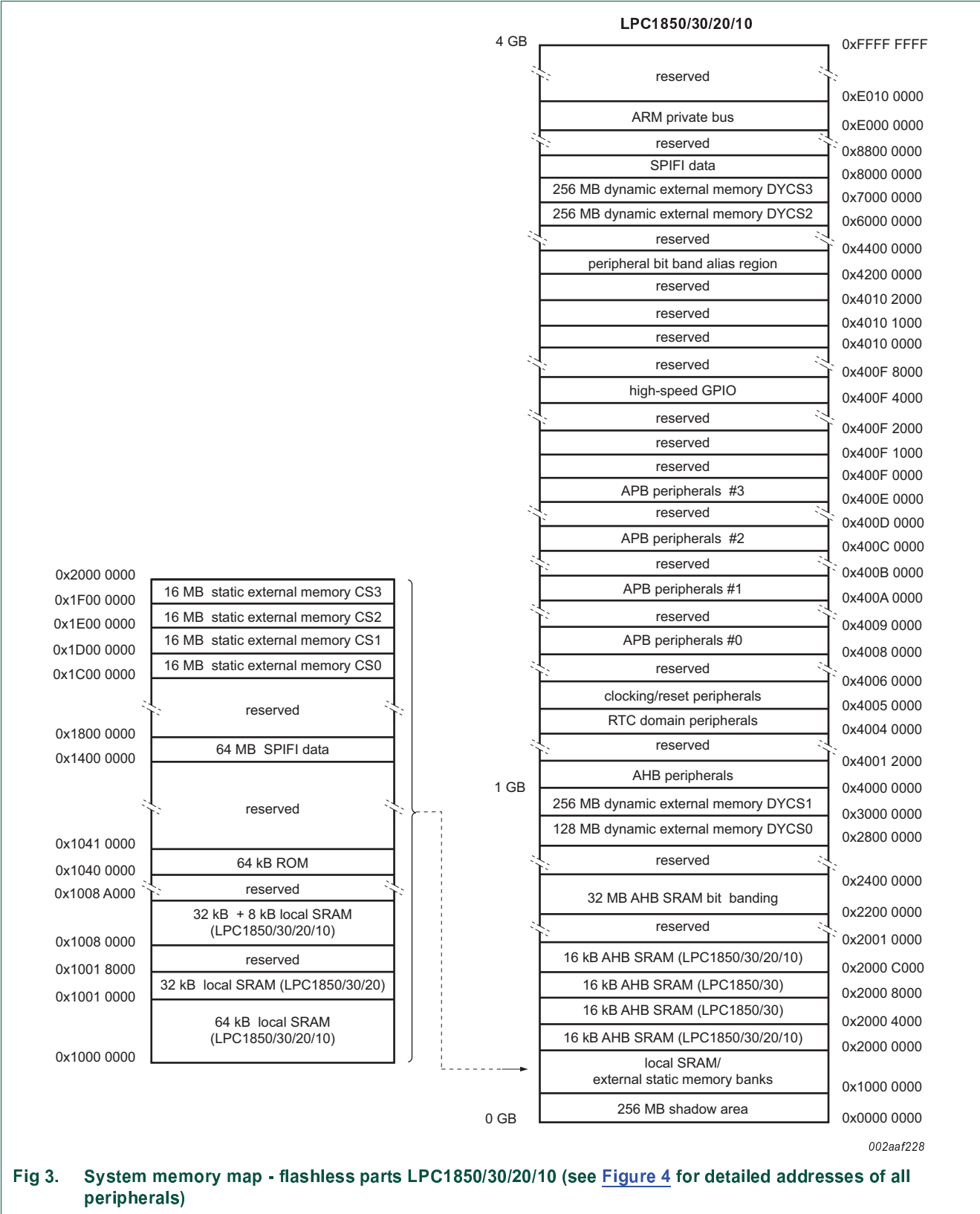


Fig 3. System memory map - flashless parts LPC1850/30/20/10 (see Figure 4 for detailed addresses of all peripherals)

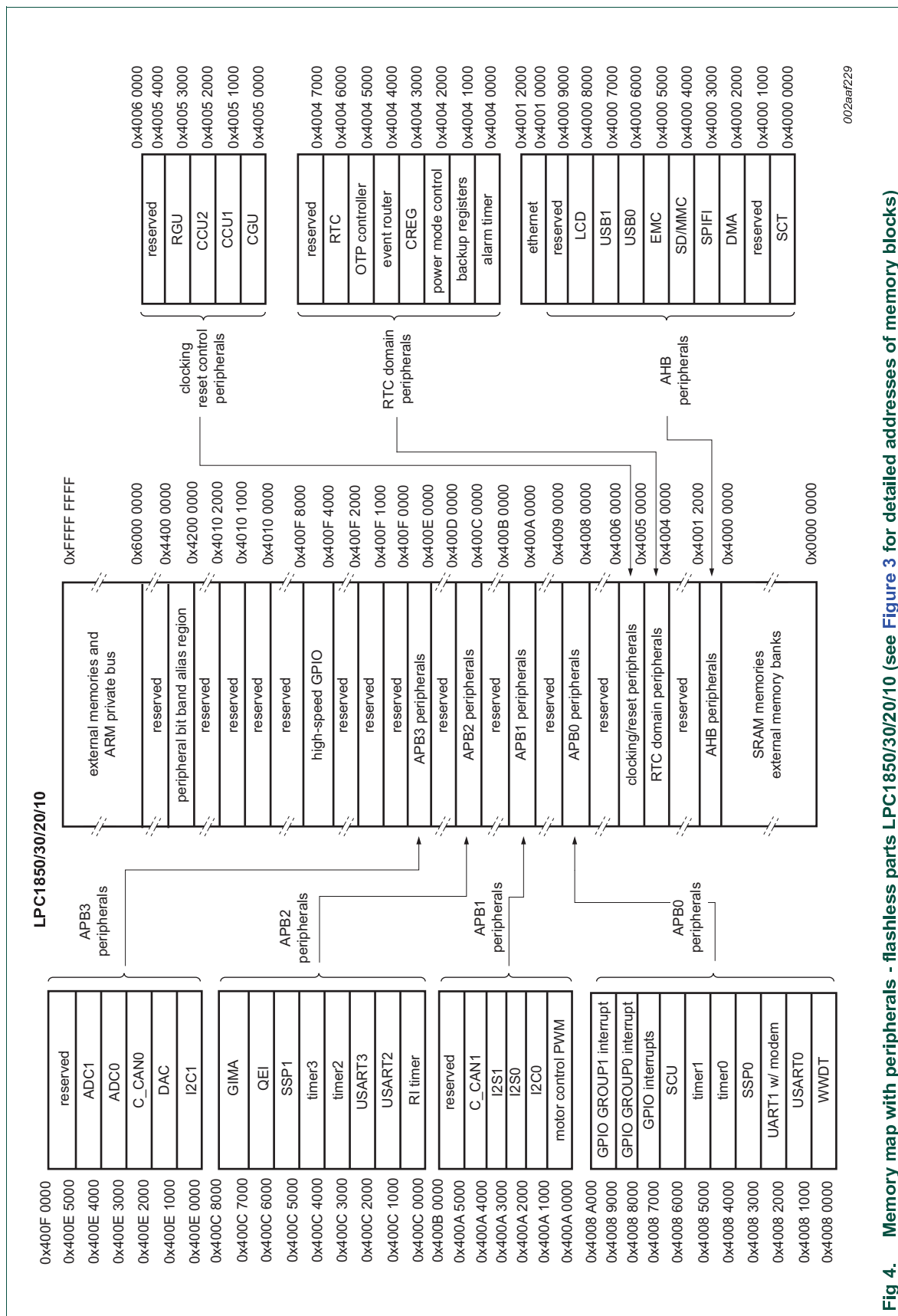
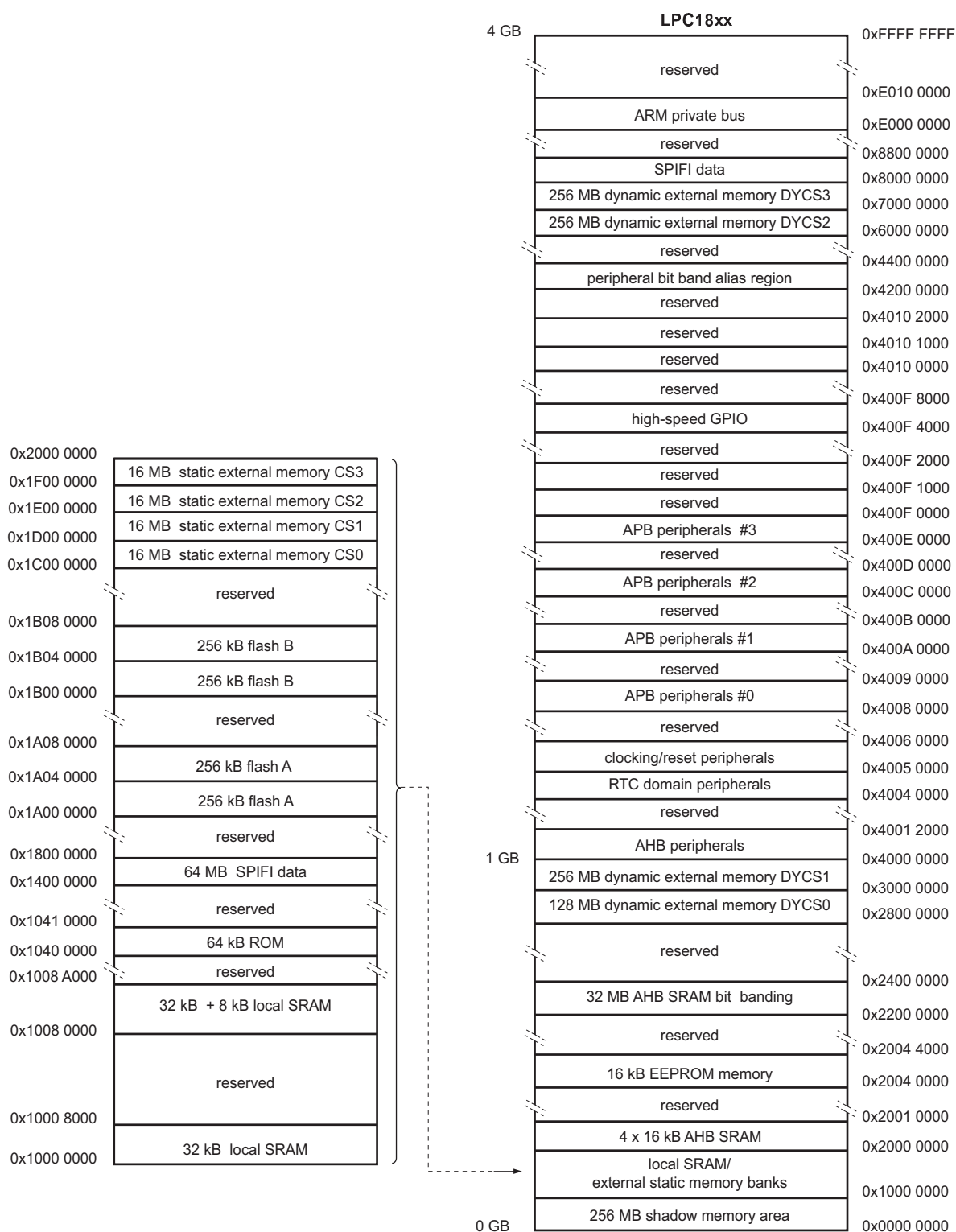


Fig 4. Memory map with peripherals - flashless parts LPC1850/30/20/10 (see Figure 3 for detailed addresses of memory blocks)



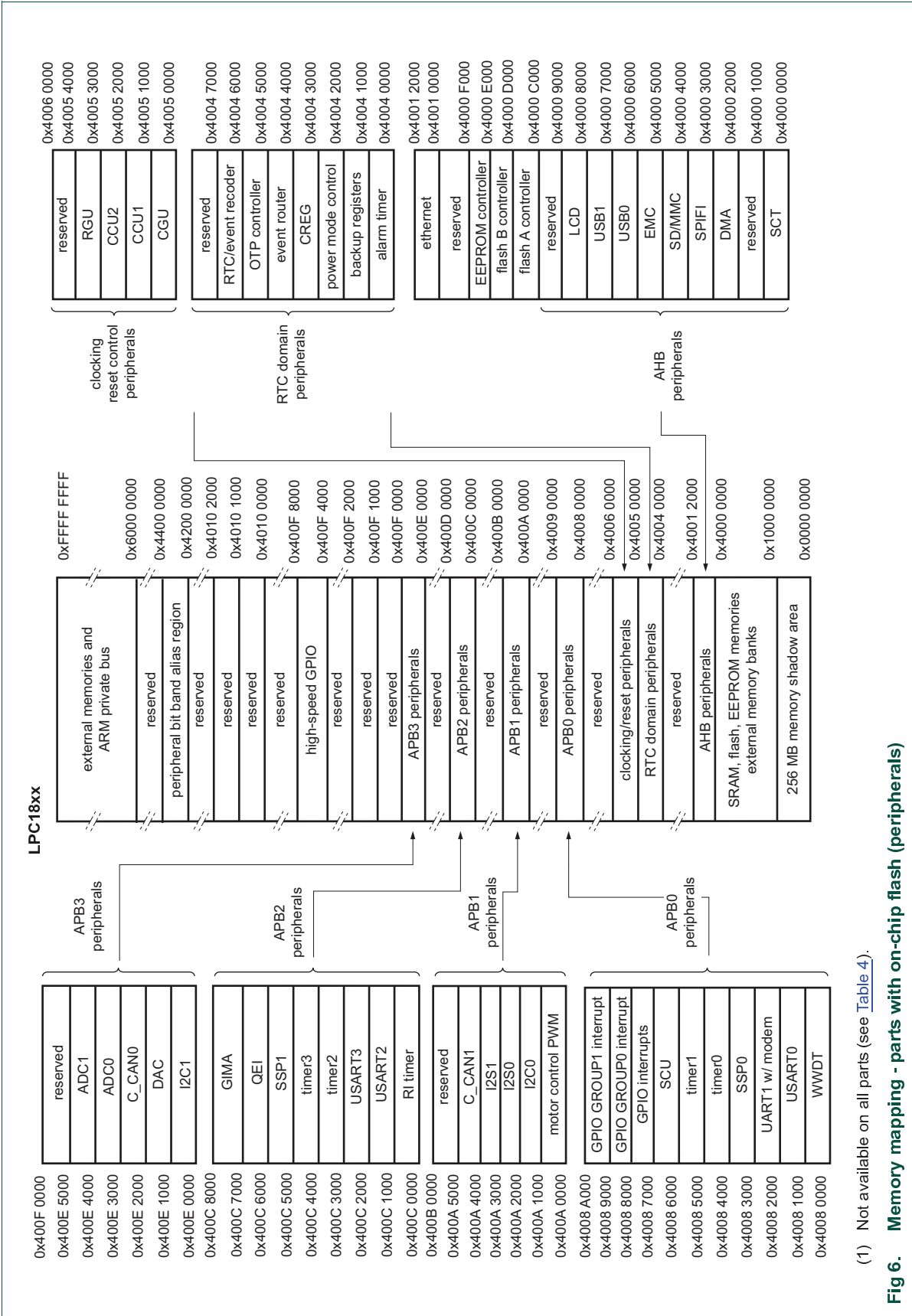


Fig 6. Memory mapping - parts with on-chip flash (peripherals)

(1) Not available on all parts (see Table 4).