

3.4 Memory map (flashless parts)

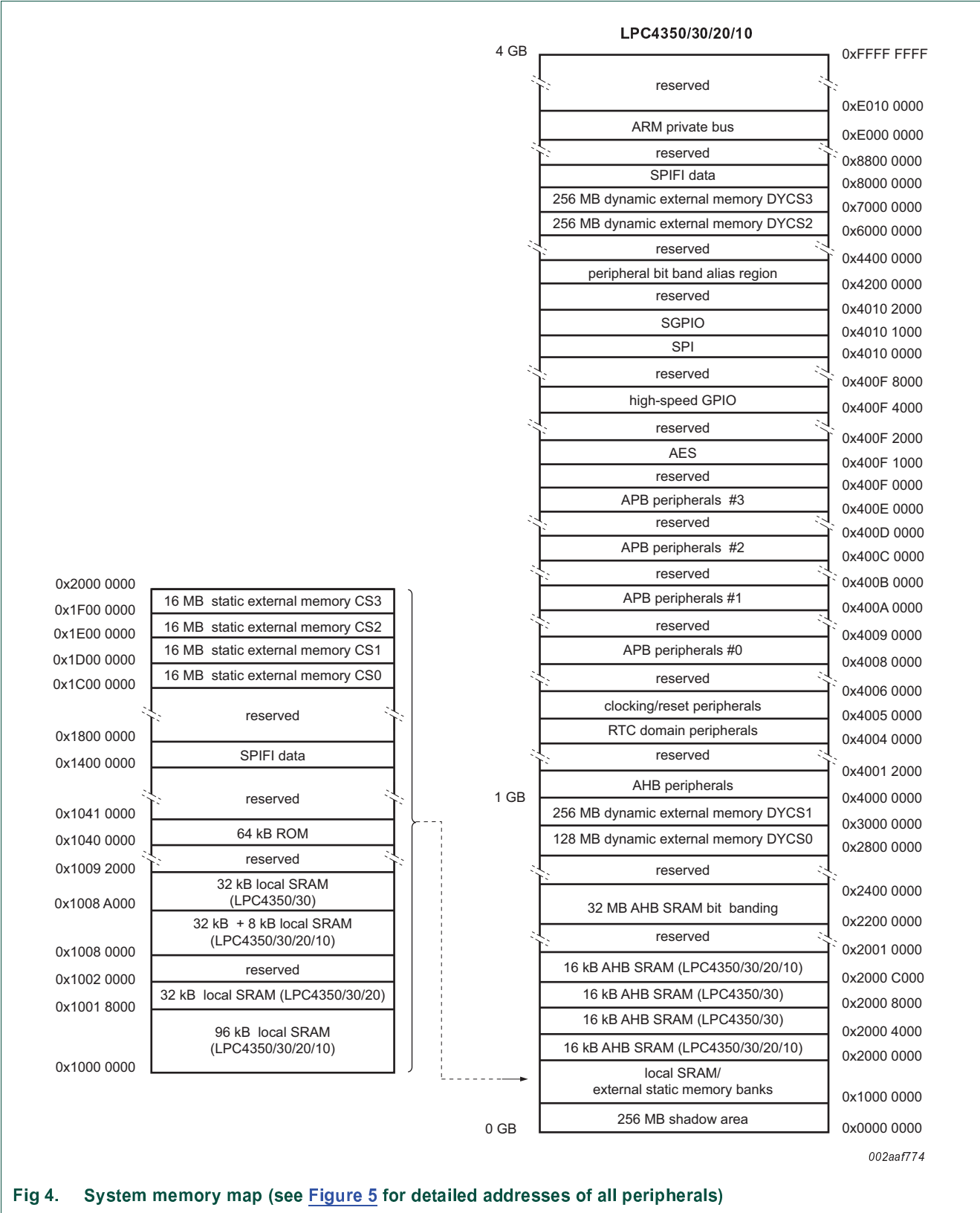


Fig 4. System memory map (see Figure 5 for detailed addresses of all peripherals)

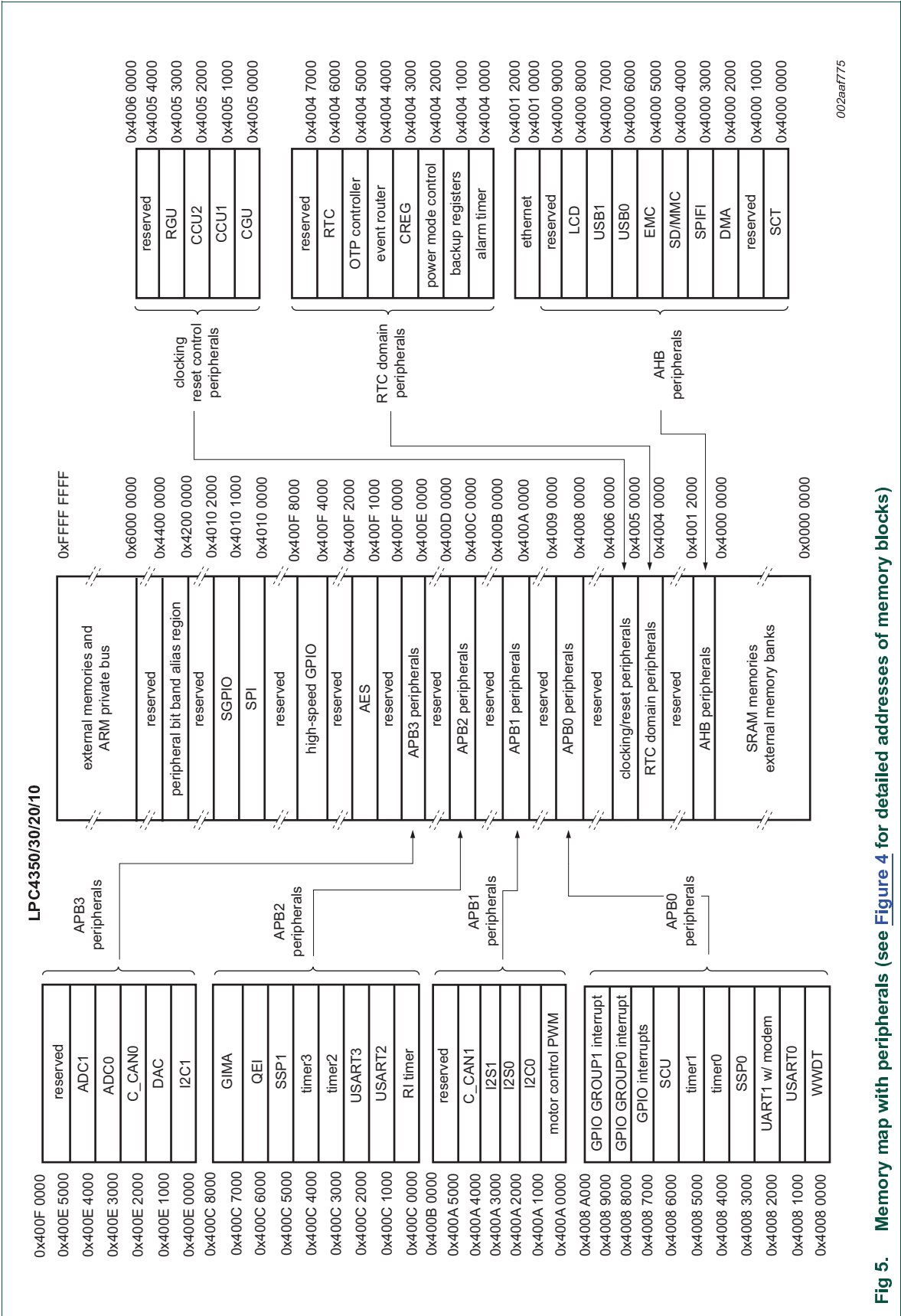


Fig. 5. Memory map with peripherals (see Figure 4 for detailed addresses of memory blocks)

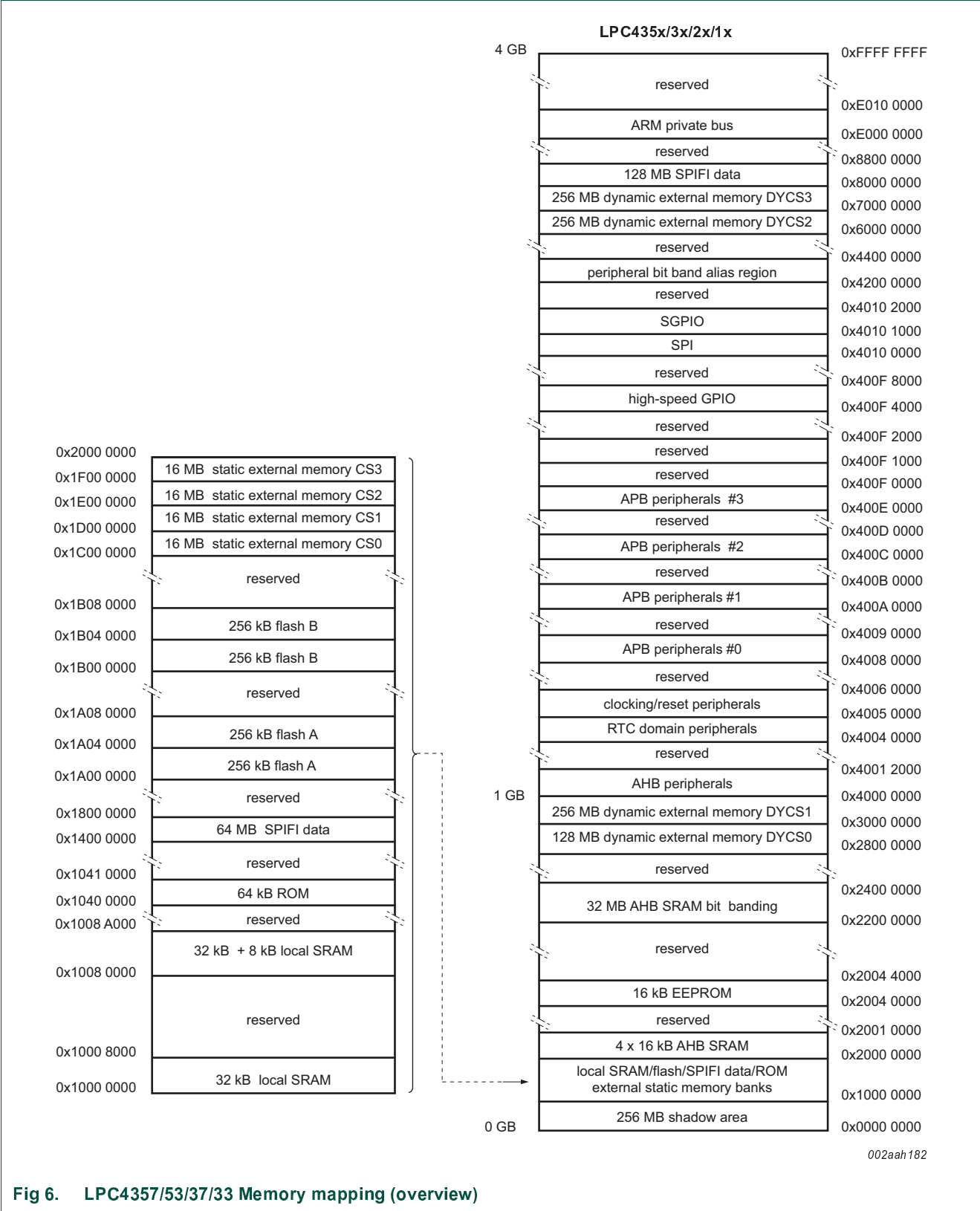


Fig 6. LPC4357/53/37/33 Memory mapping (overview)

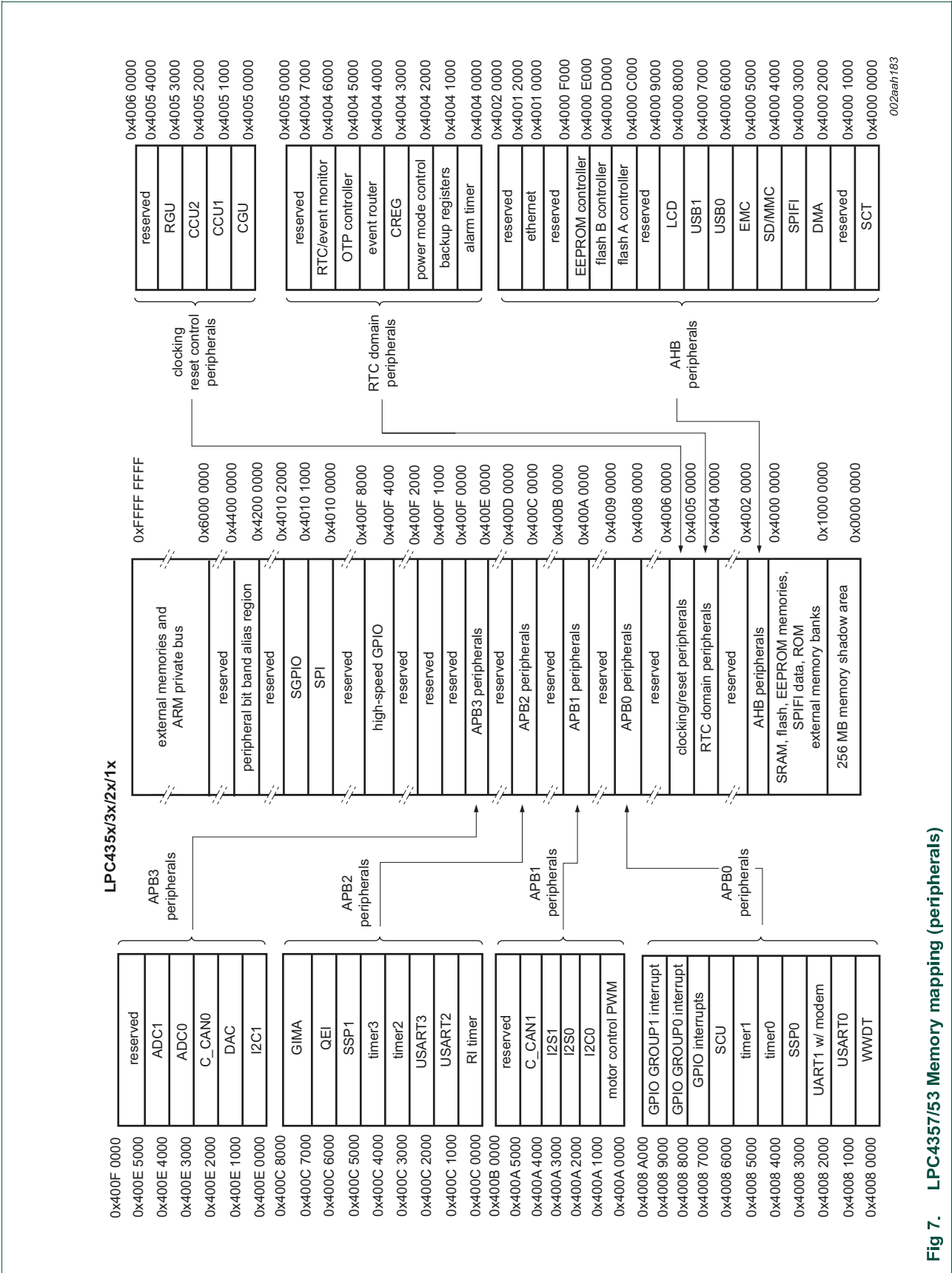


Fig. 7. LPC4357/53 Memory mapping (peripherals)