

## 2.1 Memory Map Summary

### 2.1.1 Memory Map

Figure 2-1 and Figure 2-2 show the TMS470MF04207 and TMS470MF03107 memory maps.

|             |                                       |
|-------------|---------------------------------------|
| 0xFFFFFFFF  | SYSTEM Module                         |
| 0xFFFF80000 |                                       |
| 0xFFFF7FFFF | Peripherals                           |
| 0xFF000000  |                                       |
| 0xFEFFFFFF  | PSA                                   |
| 0xFE000000  |                                       |
|             |                                       |
|             |                                       |
|             |                                       |
| 0x08405FFF  | RAM - ECC                             |
| 0x08400000  |                                       |
|             |                                       |
| 0x08105FFF  | RAM - CLR Space <sup>(A)</sup> (24KB) |
| 0x08100000  |                                       |
|             |                                       |
| 0x08085FFF  | RAM - SET Space <sup>(A)</sup> (24KB) |
| 0x08080000  |                                       |
|             |                                       |
| 0x08005FFF  | RAM (24KB)                            |
| 0x08000000  |                                       |
|             |                                       |
| 0x0047FFFF  | FLASH - ECC (Bank 1)                  |
| 0x00440000  |                                       |
| 0x0042FFFF  | FLASH - ECC (Bank 0)                  |
| 0x00400000  |                                       |
| 0x0008FFFF  | FLASH (64KB - Bank 1)                 |
| 0x00080000  |                                       |
| 0x0005FFFF  |                                       |
|             | FLASH (384KB - Bank 0)                |
| 0x00000000  |                                       |

- A. The RAM supports bit access operation which allows set/clear to dedicated bits without disturbing the other bits; for detailed description, see the Architecture Specification.

**Figure 2-1. TMS470MF04207 Memory Map**

|                                          |                                       |
|------------------------------------------|---------------------------------------|
| 0xFFFFFFFF<br>0xFFFF80000<br>0xFFFF7FFFF | SYSTEM Module                         |
| 0xFF000000<br>0xFEFFFFFF<br>0xFE000000   | Peripherals                           |
|                                          | PSA                                   |
|                                          |                                       |
| 0x08403FFF<br>0x08400000                 | RAM - ECC                             |
|                                          |                                       |
| 0x08103FFF<br>0x08100000                 | RAM - CLR Space <sup>(A)</sup> (16KB) |
|                                          |                                       |
| 0x08083FFF<br>0x08080000                 | RAM - SET Space <sup>(A)</sup> (16KB) |
|                                          |                                       |
| 0x08003FFF<br>0x08000000                 | RAM (16KB)                            |
|                                          |                                       |
| 0x00447FFF<br>0x00440000                 | FLASH - ECC (Bank 1)                  |
| 0x0041FFFF<br>0x00400000                 | FLASH - ECC (Bank 0)                  |
|                                          |                                       |
| 0x0008FFFF<br>0x00080000                 | FLASH (64KB - Bank 1)                 |
| 0x0003FFFF                               |                                       |
|                                          | FLASH (256KB - Bank 0)                |
| 0x00000000                               |                                       |

- A. The RAM supports bit access operation which allows set/clear to dedicated bits without disturbing the other bits; for detailed description, see the Architecture Specification.

**Figure 2-2. TMS470MF03107 Memory Map**